# **BUT11AX**

## **GENERAL DESCRIPTION**

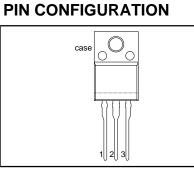
High-voltage, high-speed glass-passivated npn power transistor in a plastic full-pack envelope intended for use in converters, inverters, switching regulators, motor control systems, etc.

## QUICK REFERENCE DATA

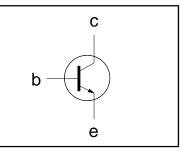
SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V <sub>CESM</sub>	Collector-emitter voltage peak value	$V_{BE} = 0 V$	-	1000	V
V <sub>CEO</sub>	Collector-emitter voltage (open base)		-	450	V
I <sub>C</sub>	Collector current (DC)		-	5	A
I <sub>CM</sub>	Collector current peak value		-	10	A
P <sub>tot</sub>	Total power dissipation	T <sub>hs</sub> ≤ 25 °C	-	32	W
V <sub>CEsat</sub>	Collector-emitter saturation voltage		-	1.5	V
I <sub>Csat</sub>	Collector saturation current		2.5	-	A
t <sub>f</sub>	Fall time		150	-	ns

## **PINNING - SOT186A**

# PINDESCRIPTION1base2collector3emittercaseisolated



## SYMBOL



## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CESM</sub>	Collector-emitter voltage peak value	$V_{BE} = 0 V$	-	1000	V
V <sub>CEO</sub>	Collector-emitter voltage (open base)		-	450	
I <sub>c</sub>	Collector current (DC)		-	5	A
I <sub>CM</sub>	Collector current peak value		-	10	A
I <sub>B</sub>	Base current (DC)		-	2	A
I <sub>BM</sub>	Base current peak value		-	4	A
I <sub>BM</sub> P <sub>tot</sub>	Total power dissipation	$T_{hs} \le 25 \degree C$	-	32	W
T <sub>stq</sub>	Storage temperature		-65	150	°C
Tj	Junction temperature		-	150	°C

## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R <sub>th j-hs</sub>	Junction to heatsink	with heatsink compound	-	3.95	K/W
R <sub>th j-a</sub>	Junction to ambient	in free air	55	-	K/W

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## **ISOLATION LIMITING VALUE & CHARACTERISTIC**

 $T_{hs} = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>isol</sub>	R.M.S. isolation voltage from all three terminals to external heatsink	f = 50-60 Hz; sinusoidal waveform; R.H. ≤ 65% ; clean and dustfree	-		2500	V
C <sub>isol</sub>	Capacitance from T2 to external heatsink	f = 1 MHz	-	10	-	pF

## STATIC CHARACTERISTICS

 $T_{hs}$  = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CES</sub>	Collector cut-off current <sup>1</sup>		-	-	1.0	mA
I <sub>CES</sub>		$V_{BE} = 0 V; V_{CE} = V_{CESMmax};$ T <sub>i</sub> = 125 °C	-	-	2.0	mA
I <sub>EBO</sub>	Emitter cut-off current	$V_{EB} = 9 \text{ V}; I_{C} = 0 \text{ A}$	-	-	10	mA
V <sub>CEOsust</sub>	Collector-emitter sustaining voltage	$I_{B} = 0 \text{ A}; I_{C} = 100 \text{ mA};$	450	-	-	V
V <sub>CEsat</sub>	Collector-emitter saturation voltages	L = 25 mH I <sub>C</sub> = 2.5 A; I <sub>B</sub> = 0.5 A	_	_	1.5	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	$I_{C} = 2.5 \text{ A}; I_{B} = 0.5 \text{ A}$	-	-	1.3	Ň
h <sub>FE</sub>	DC current gain	$I_{c} = 5 \text{ mA}; V_{ce} = 5 \text{ V}$	10	18	35	
h <sub>FE</sub>		$I_{c} = 500 \text{ mA}; V_{ce} = 5 \text{ V}$	10	20	35	

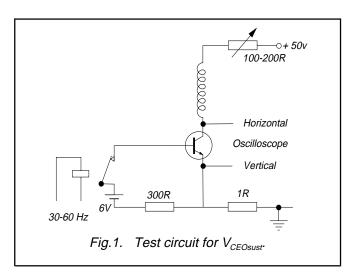
#### **DYNAMIC CHARACTERISTICS**

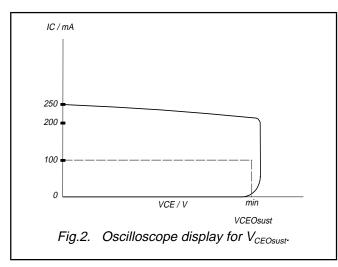
 $T_{hs}$  = 25 °C unless otherwise specified

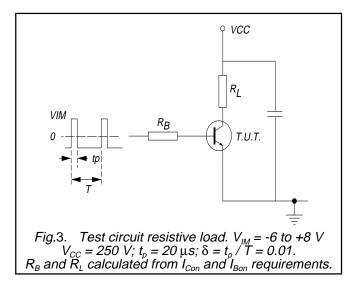
SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
t <sub>on</sub> t <sub>s</sub> t <sub>f</sub>	Switching times (resistive load) Turn-on time Turn-off storage time Turn-off fall time	$I_{Con} = 2.5 \text{ A}; I_{Bon} = -I_{Boff} = 0.5 \text{ A}$	0.6 3.5 0.6	- -	μs μs μs
t <sub>s</sub> t <sub>f</sub>	Switching times (inductive load) Turn-off storage time Turn-off fall time		1.5 150	- -	μs ns
t <sub>s</sub> t <sub>f</sub>	Switching times (inductive load) Turn-off storage time Turn-off fall time	$      I_{Con} = 2.5 \text{ A}; I_{Bon} = 0.5 \text{ A}; L_{B} = 1  \mu\text{H}; \\ -V_{BB} = 5 \text{ V}; T_{j} = 100 ^{\circ}\text{C} $	1.8 170	- -	μs ns

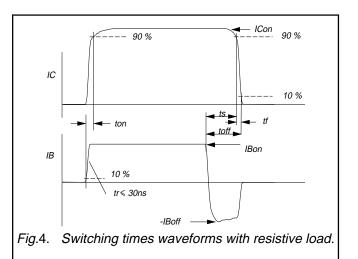
<sup>1</sup> Measured with half sine-wave voltage (curve tracer).

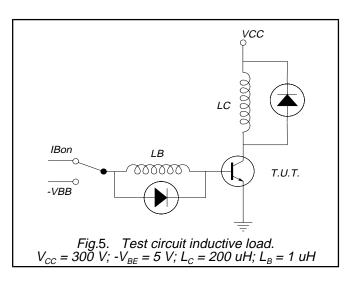
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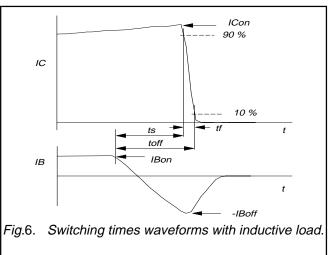




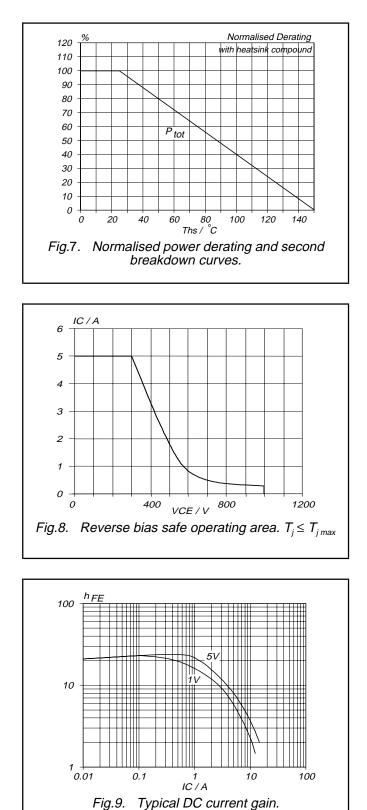




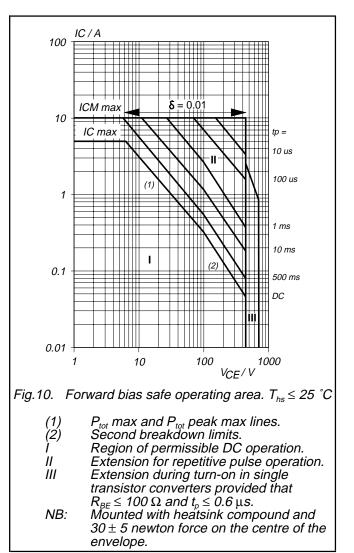




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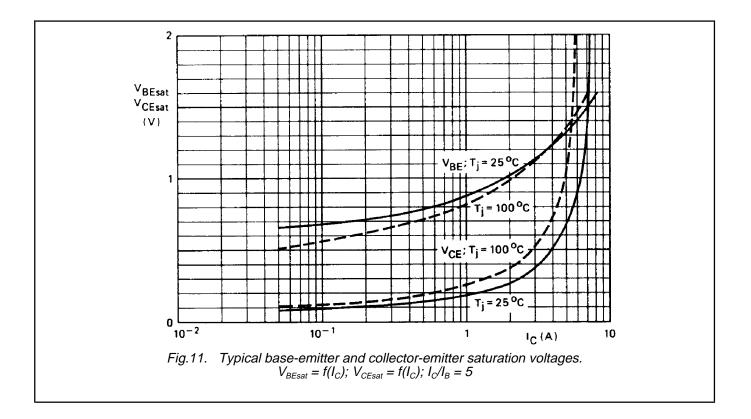


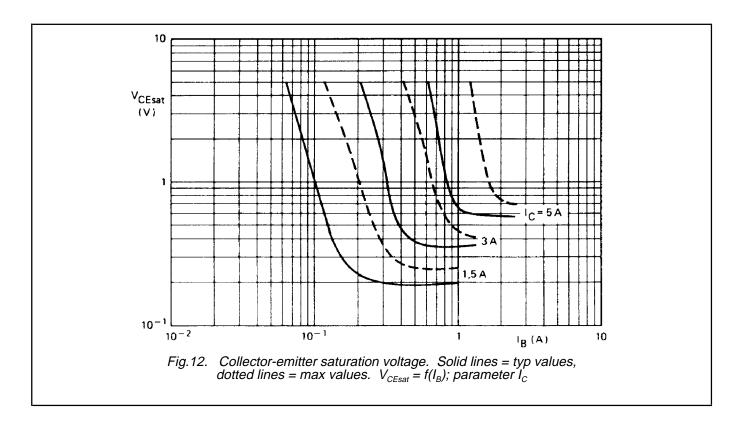
 $h_{FE} = f(I_C)$ ; parameter  $V_{CE}$ 



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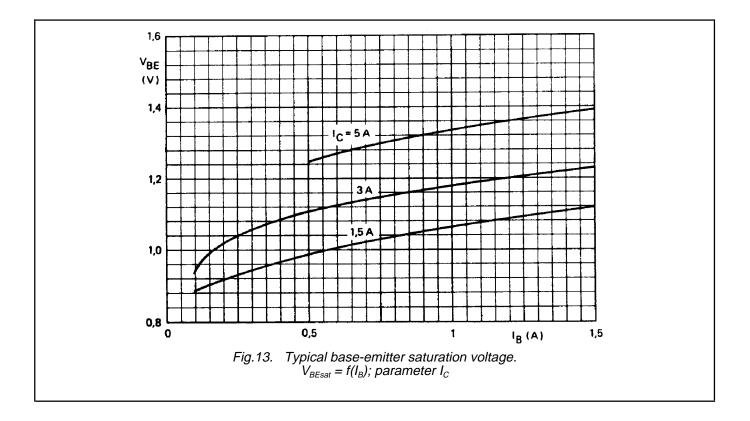
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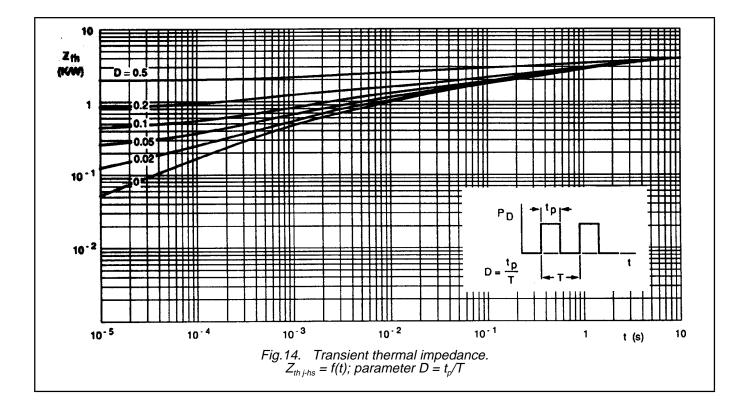




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## Silicon Diffused Power Transistor



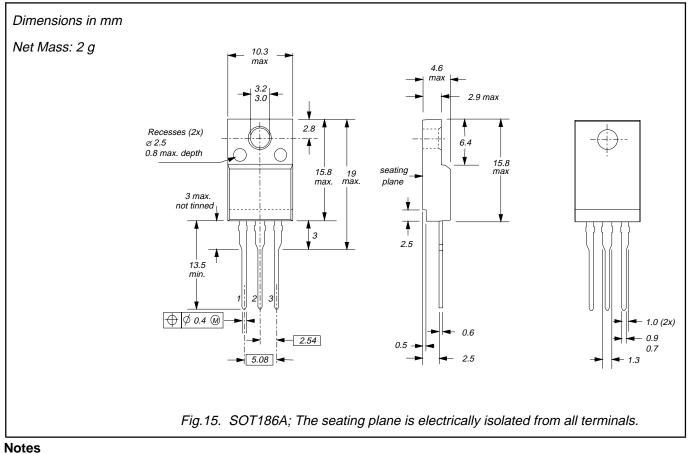


#### November 1995

## Product specification

## **BUT11AX**

## **MECHANICAL DATA**



Refer to mounting instructions for F-pack envelopes.
Epoxy meets UL94 V0 at 1/8".

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#### DEFINITIONS

Data sheet status				
Objective specification	Dijective specification This data sheet contains target or goal specifications for product development.			
Preliminary specification	minary specification This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
Limiting values				
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above on or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.				
Application information				
Where application information is given, it is advisory and does not form part of the specification.				
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